

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of Applicants:

M. Ueda

Serial No.: 09/682,138

Filed: August 2, 2001

For: MULTIPROCESSOR SYSTEM, PROCESSOR MODULE FOR USE THEREIN,
AND TASK ALLOCATION METHOD IN MULTIPROCESSING



Date: September 6, 2001

Group Art Unit: 2185

Examiner:

Docket No: JP920000046US1

Assistant Commissioner for Patents
Washington, D. C. 20231

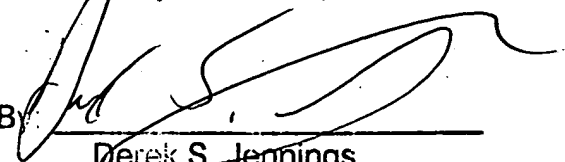
PRELIMINARY AMENDMENT

IN THE TITLE

In the title of the Specification, delete "MULTIPROCESSOR SYSTEM,
PROCESSOR MODULE FOR USE THEREIN, AND TASK ALLOCATION METHOD IN
MULTIPROCESSING", replace therefore --PROCESSOR MODULE FOR A
MULTIPROCESSOR SYSTEM AND TASK ALLOCATION METHOD THEREOF--.

Please charge any fee necessary to enter this paper and any previous paper to
deposit account 09-0468.

Respectfully submitted,

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